

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Canceled)
2. (Canceled)
3. (Canceled)
4. (Canceled)
5. (Currently Amended) A liquid crystal device ~~in accordance with claim 4,~~

having multiple pixels, the liquid crystal device comprising:

an input terminal that receives a display signal including a predetermined signal used for generating a common signal and multiple pixel signals to be supplied to the multiple pixels, wherein the predetermined signal is embedded into a predetermined period between a group of pixel signals and another group of pixel signals in the display signal, and the common signal is to be commonly supplied to the multiple pixels;

a common signal line that is commonly connected to the multiple pixels; and

a common signal line driving circuit that generates the common signal in response to the predetermined signal included in the display signal input from the input terminal and supplies the generated common signal to the common signal line,

wherein the common signal line driving circuit is a sample/hold circuit that samples the predetermined signal included in the display signal input from the input terminal and outputs the predetermined sampled signal as the common signal.

6. (Original) A liquid crystal device in accordance with claim 5, the liquid crystal device further comprising:

multiple rows of scanning lines and multiple columns of signal lines for selecting the multiple pixels;

a scanning line driving circuit that supplies scanning signals to the corresponding multiple rows of scanning lines in a sequence of the multiple rows of scanning lines; and

a signal line driving circuit that samples display signals corresponding to the multiple columns of signal lines in a sequence of the multiple columns of signal lines and supplies the sampled display signals to the corresponding signal lines,

wherein the signal line driving circuit supplies a sample/hold signal, which is used for sampling the predetermined signal, to the common signal line driving circuit.

7. (Original) A liquid crystal device in accordance with claim 5, the liquid crystal device further comprising:

multiple rows of scanning lines and multiple columns of signal lines for selecting the multiple pixels;

a scanning line driving circuit that supplies scanning signals to the corresponding multiple rows of scanning lines in a sequence of the multiple rows of scanning lines; and

a signal line driving circuit that samples display signals corresponding to the multiple columns of signal lines in a sequence of the multiple columns of signal lines and supplies the sampled display signals to the corresponding signal lines,

wherein the scanning line driving circuit supplies a sample/hold signal, which is used for sampling the predetermined signal, to the common signal line driving circuit.

8. (Canceled)

9. (Currently Amended) A liquid crystal device ~~in accordance with claim 8,~~  
having multiple pixels, the liquid crystal device comprising:

an input terminal that receives a display signal including a predetermined signal used for generating a common signal and multiple pixel signals to be supplied to the multiple pixels, wherein the predetermined signal is embedded into a predetermined period

between a group of pixel signals and another group of pixel signals in the display signal, and the common signal is to be commonly supplied to the multiple pixels;

\_\_\_\_\_ a common signal line that is commonly connected to the multiple pixels; and

\_\_\_\_\_ a common signal line driving circuit that generates the common signal in response to the predetermined signal included in the display signal input from the input terminal and supplies the generated common signal to the common signal line,

\_\_\_\_\_ wherein the predetermined period is part of a horizontal scanning period except an effective horizontal scan period, and

\_\_\_\_\_ wherein the common signal line driving circuit is a sample/hold circuit that samples the predetermined signal included in the display signal input from the input terminal and outputs the predetermined sampled signal as the common signal.

10. (Original) A liquid crystal device in accordance with claim 9, the liquid crystal device further comprising:

multiple rows of scanning lines and multiple columns of signal lines for selecting the multiple pixels;

a scanning line driving circuit that supplies scanning signals to the corresponding multiple rows of scanning lines in a sequence of the multiple rows of scanning lines; and

a signal line driving circuit that samples display signals corresponding to the multiple columns of signal lines in a sequence of the multiple columns of signal lines and supplies the sampled display signals to the corresponding signal lines,

wherein the signal line driving circuit supplies a sample/hold signal, which is used for sampling the predetermined signal, to the common signal line driving circuit.

11. (Original) A liquid crystal device in accordance with claim 9, the liquid crystal device further comprising:

multiple rows of scanning lines and multiple columns of signal lines for selecting the multiple pixels;

a scanning line driving circuit that supplies scanning signals to the corresponding multiple rows of scanning lines in a sequence of the multiple rows of scanning lines; and

a signal line driving circuit that samples display signals corresponding to the multiple columns of signal lines in a sequence of the multiple columns of signal lines and supplies the sampled display signals to the corresponding signal lines,

wherein the scanning line driving circuit supplies a sample/hold signal, which is used for sampling the predetermined signal, to the common signal line driving circuit.

12. (Canceled)

13. (Currently Amended) A liquid crystal device ~~in accordance with claim 12~~ having multiple pixels, the liquid crystal device comprising:

an input terminal that receives a display signal including a predetermined signal used for generating a common signal and multiple pixel signals to be supplied to the multiple pixels, wherein the predetermined signal is embedded into a predetermined period between a group of pixel signals and another group of pixel signals in the display signal, and the common signal is to be commonly supplied to the multiple pixels;

a common signal line that is commonly connected to the multiple pixels; and

a common signal line driving circuit that generates the common signal in response to the predetermined signal included in the display signal input from the input terminal and supplies the generated common signal to the common signal line,

\_\_\_\_\_ wherein the common signal line driving circuit is a sample/hold circuit that samples the predetermined signal included in the display signal input from the input terminal and outputs the predetermined sampled signal as the common signal.

14. (Original) A liquid crystal device in accordance with claim 13, the liquid crystal device further comprising:

multiple rows of scanning lines and multiple columns of signal lines for selecting the multiple pixels;

a scanning line driving circuit that supplies scanning signals to the corresponding multiple rows of scanning lines in a sequence of the multiple rows of scanning lines; and

a signal line driving circuit that samples display signals corresponding to the multiple columns of signal lines in a sequence of the multiple columns of signal lines and supplies the sampled display signals to the corresponding signal lines,

wherein the signal line driving circuit supplies a sample/hold signal, which is used for sampling the predetermined signal, to the common signal line driving circuit.

15. (Original) A liquid crystal device in accordance with claim 13, the liquid crystal device further comprising:

multiple rows of scanning lines and multiple columns of signal lines for selecting the multiple pixels;

a scanning line driving circuit that supplies scanning signals to the corresponding multiple rows of scanning lines in a sequence of the multiple rows of scanning lines; and

a signal line driving circuit that samples display signals corresponding to the multiple columns of signal lines in a sequence of the multiple columns of signal lines and supplies the sampled display signals to the corresponding signal lines,

wherein the scanning line driving circuit supplies a sample/hold signal, which is used for sampling the predetermined signal, to the common signal line driving circuit.

16. (Canceled)

17. (Previously Presented) An image display apparatus, comprising:  
a liquid crystal device having multiple pixels; and  
an image processing device that generates a display signal, which is to be input into the liquid crystal device,

the liquid crystal device comprising:

an input terminal that receives the display signal including a predetermined signal used for generating a common signal and multiple pixel signals to be supplied to the multiple pixels, wherein the predetermined signal is embedded into a predetermined period between a group of pixel signals and another group of pixel signals in the display signal, and the common signal is to be commonly supplied to the multiple pixels,

the image processing device comprising:

a video signal conversion circuit that converts an input video signal and generates the multiple pixel signals, which are to be given to the multiple pixels of the liquid crystal device; and

a display signal generation circuit that combines the multiple pixel signals with a predetermined signal, which is used for generating a common signal to be commonly supplied to the multiple pixels, and thereby generates one display signal.

18. (Original) An image display apparatus in accordance with claim 17, wherein the image processing device further comprises:

an adjustment control circuit that adjusts a value of data included in the predetermined signal.

19. (Canceled)

20. (Canceled)